

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bratin Saha      Art Unit: 2183  
Serial No.: 10/797,886      Examiner: Brian P. Johnson  
Filed: March 9, 2004      Assignee: Intel Corporation

Title: SYNCHRONIZATION OF PARALLEL PROCESSES USING SPECULATIVE  
EXECUTION OF SYNCHRONIZATION INSTRUCTIONS

**Mail Stop RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO ACTION OF MARCH 7, 2007

This paper is filed in reply to the Office Action mailed  
March 7, 2007. Please consider the following.

Amendments to the Claims are reflected in the listing of  
claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.